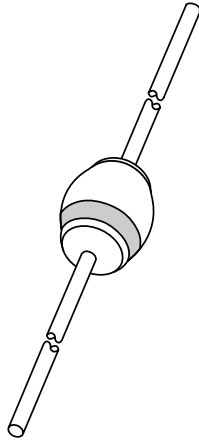


# DATA SHEET



## **BYV99**

**Ultra fast low-loss controlled  
avalanche rectifier**

Product specification  
Supersedes data of 1996 Feb 19

2003 Mar 04

## Ultra fast low-loss controlled avalanche rectifier

BYV99

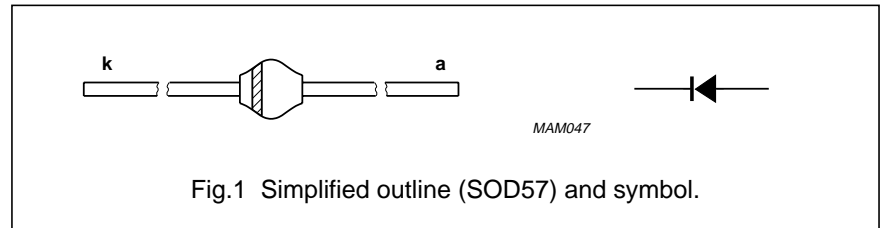
## FEATURES

- Glass passivated
- Low leakage current
- Excellent stability
- Guaranteed avalanche energy absorption capability
- Available in ammo-pack.

## DESCRIPTION

Rugged glass SOD57 package, using a high temperature alloyed construction.

This package is hermetically sealed and fatigue free as coefficients of expansion of all used parts are matched.



## LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{RRM}$	repetitive peak reverse voltage		–	600	V
$V_R$	continuous reverse voltage		–	600	V
$I_{F(AV)}$	average forward current	$T_{tp} = 50\text{ °C}$ ; lead length = 10 mm; see Fig.2; averaged over any 20 ms period; see also Fig.6	–	1	A
		$T_{amb} = 60\text{ °C}$ ; see Fig.3; PCB mounting (see Fig.11); averaged over any 20 ms period; see also Fig.6	–	0.55	A
$I_{FRM}$	repetitive peak forward current	$T_{tp} = 50\text{ °C}$ ; see Fig.4	–	9	A
		$T_{amb} = 60\text{ °C}$ ; see Fig.5	–	5	A
$I_{FSM}$	non-repetitive peak forward current	$t = 10\text{ ms}$ half sine wave; $T_j = T_{j(max)}$ prior to surge; $V_R = V_{RRMmax}$	–	20	A
$E_{RSM}$	non-repetitive peak reverse avalanche energy	$L = 120\text{ mH}$ ; $T_j = T_{j(max)}$ prior to surge; inductive load switched off	–	10	mJ
$T_{stg}$	storage temperature		–65	+175	°C
$T_j$	junction temperature	see also Fig.10	–65	+150	°C

## Ultra fast low-loss controlled avalanche rectifier

BYV99

**ELECTRICAL CHARACTERISTICS** $T_j = 25\text{ °C}$  unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_F$	forward voltage	$I_F = 1\text{ A}$ ; $T_j = T_{j(\text{max})}$ ; see Fig.7	–	–	1.5	V
		$I_F = 1\text{ A}$ ; see Fig.7	–	–	2.7	V
$V_{(BR)R}$	reverse avalanche breakdown voltage	$I_R = 0.1\text{ mA}$	700	–	–	V
$I_R$	reverse current	$V_R = V_{RRM\text{max}}$ ; see Fig.8	–	–	5	$\mu\text{A}$
		$V_R = V_{RRM\text{max}}$ ; $T_j = 150\text{ °C}$ ; see Fig.8	–	–	75	$\mu\text{A}$
$t_{rr}$	reverse recovery time	when switched from $I_F = 0.5\text{ A}$ to $I_R = 1\text{ A}$ ; measured at $I_R = 0.25\text{ A}$ ; see Fig.13	–	–	15	ns
$C_d$	diode capacitance	$f = 1\text{ MHz}$ ; $V_R = 0\text{ V}$ ; see Fig.9	–	75	–	pF
$\left  \frac{dI_R}{dt} \right $	maximum slope of reverse recovery current	when switched from $I_F = 1\text{ A}$ to $V_R \geq 30\text{ V}$ and $dI_F/dt = -1\text{ A}/\mu\text{s}$ ; see Fig.12	–	–	3	$\text{A}/\mu\text{s}$

**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j\text{-tp}}$	thermal resistance from junction to tie-point	lead length = 10 mm	46	K/W
$R_{th\ j\text{-a}}$	thermal resistance from junction to ambient	note 1	100	K/W

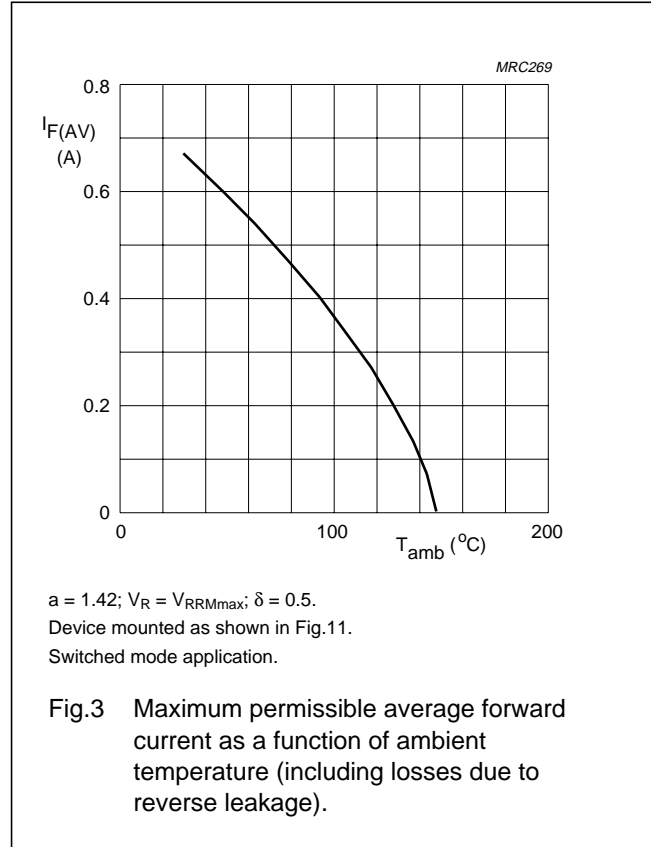
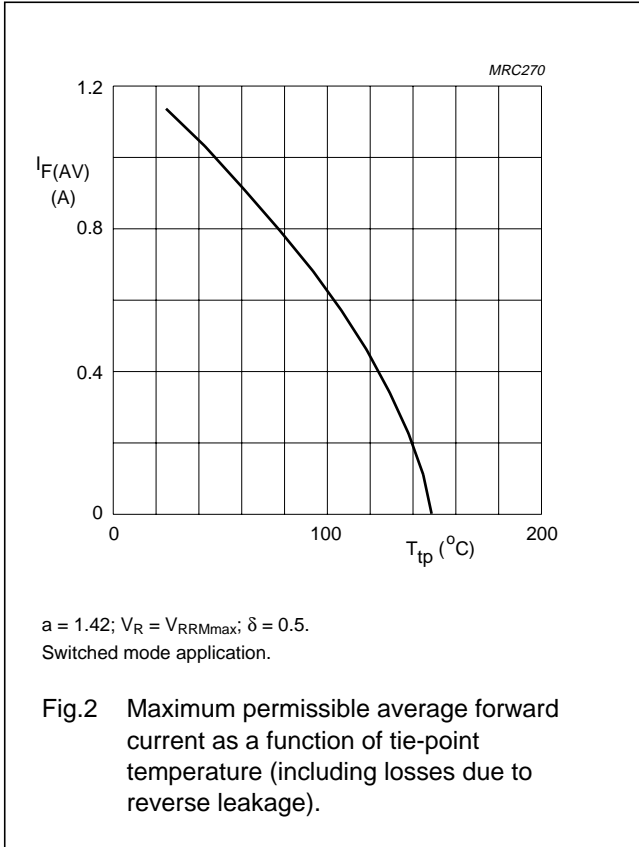
**Note**

1. Device mounted on an epoxy-glass printed-circuit board, 1.5 mm thick; thickness of Cu-layer  $\geq 40\ \mu\text{m}$ ; see Fig.11. For more information please refer to the "General Part of associated Handbook".

Ultra fast low-loss controlled avalanche rectifier

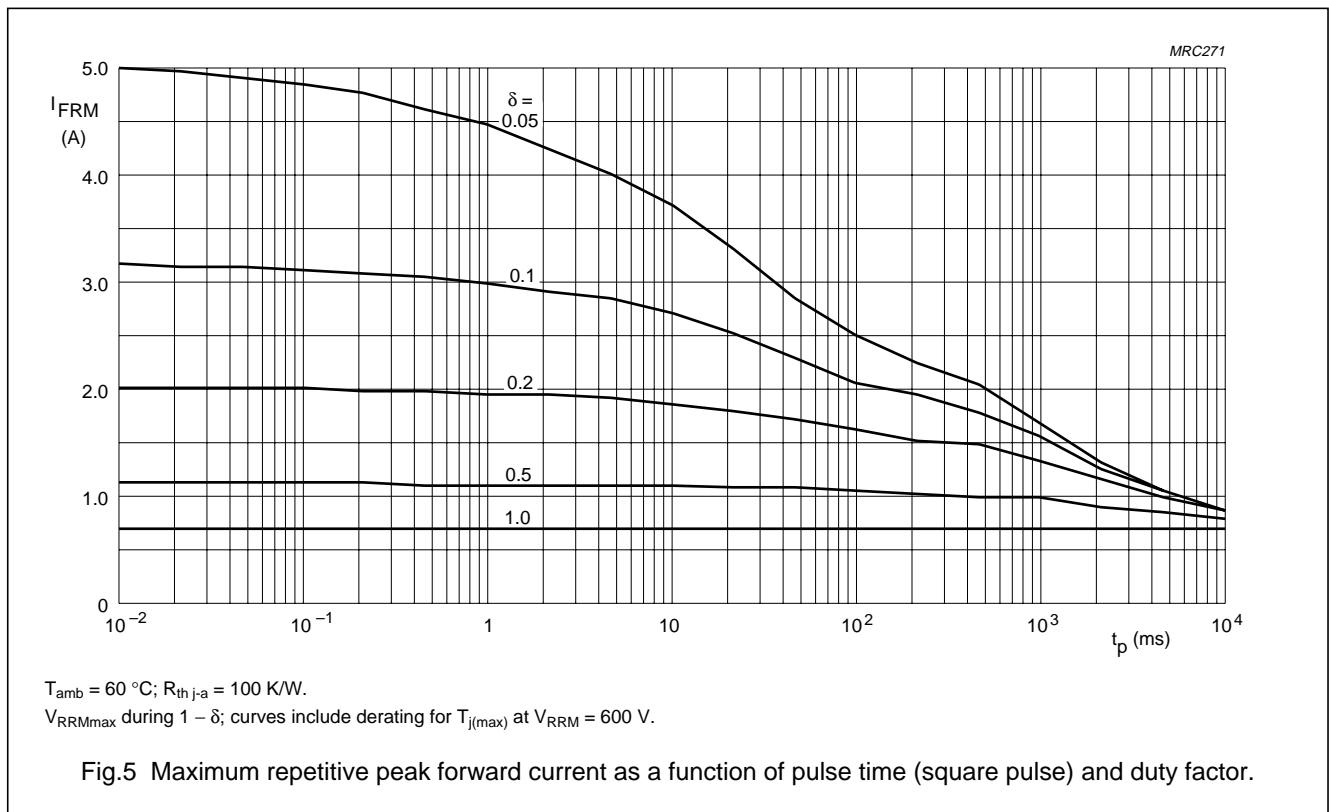
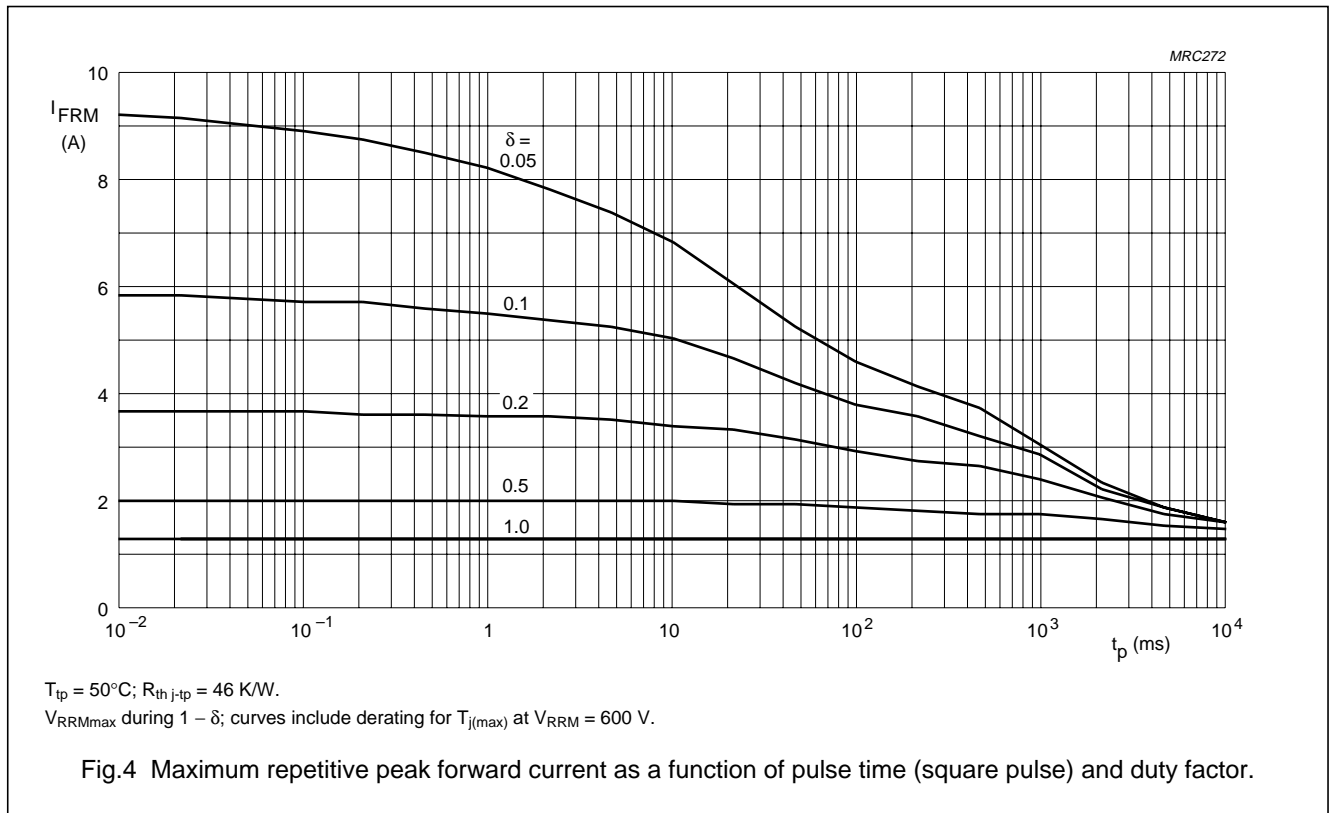
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GRAPHICAL DATA



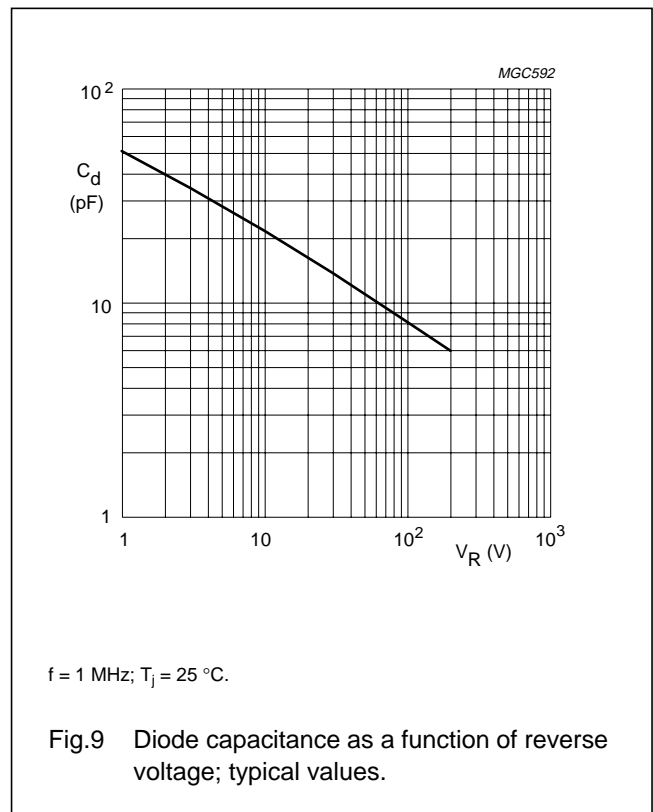
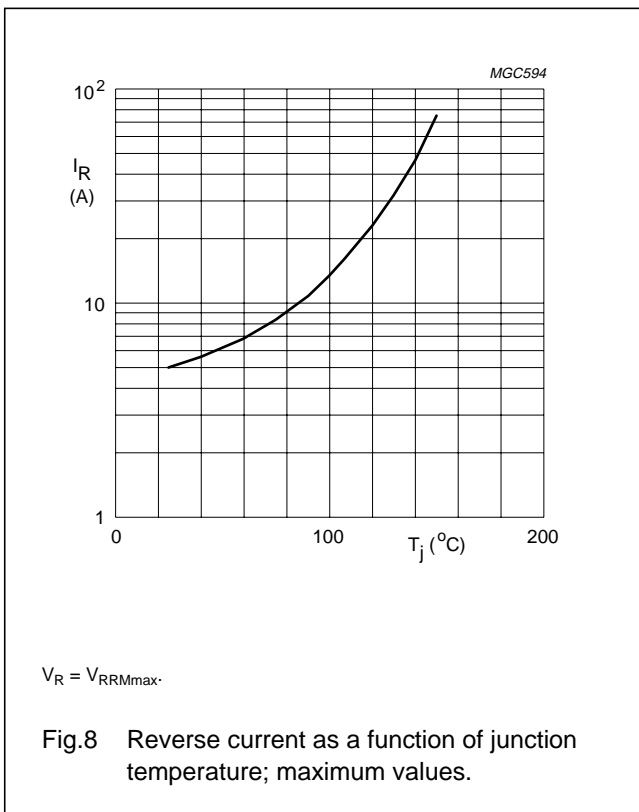
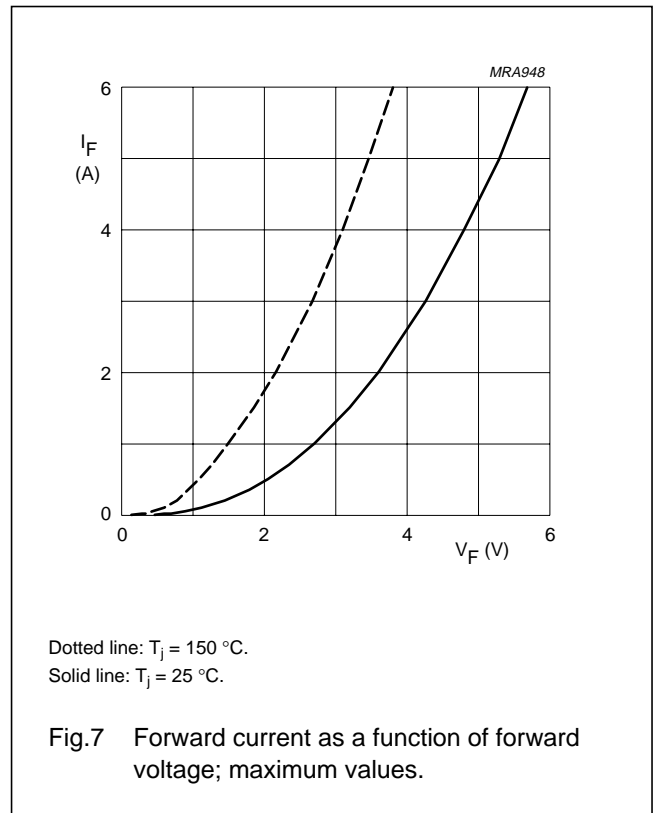
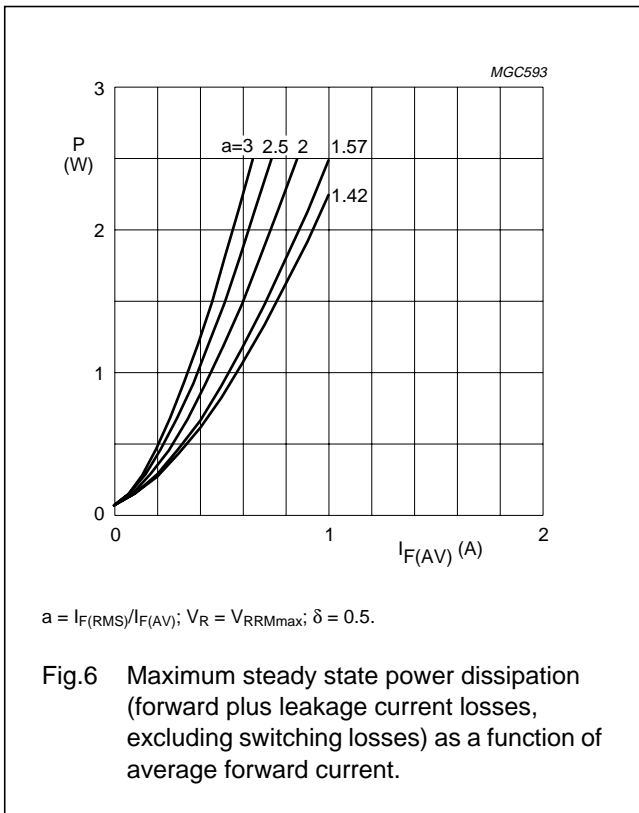
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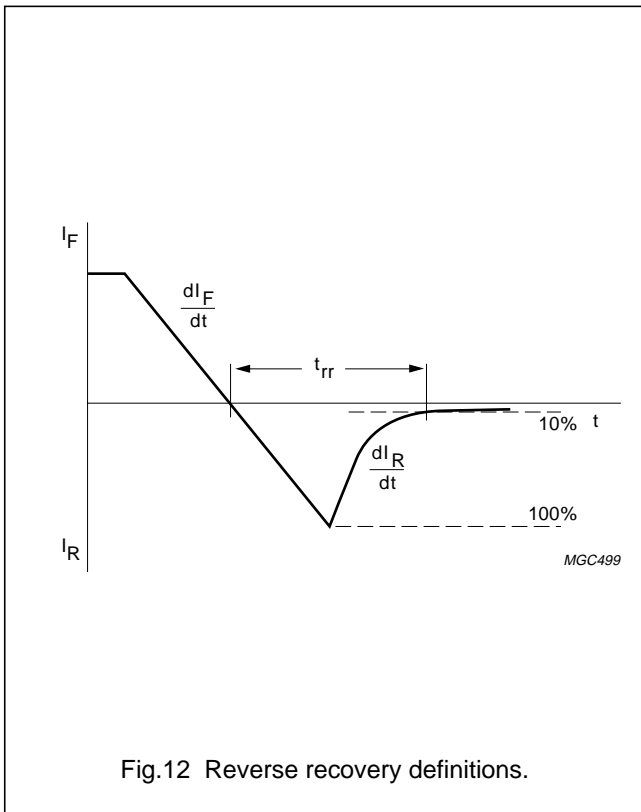
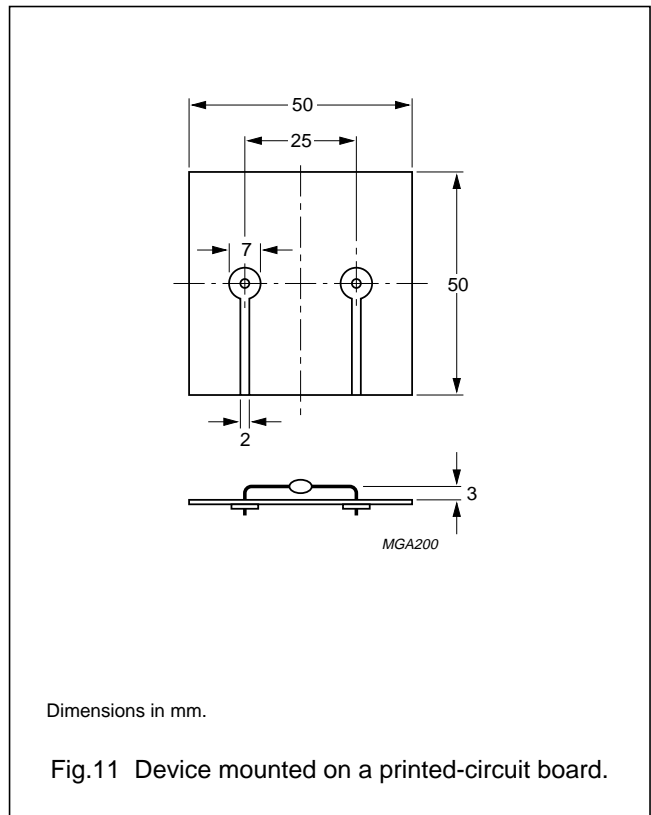
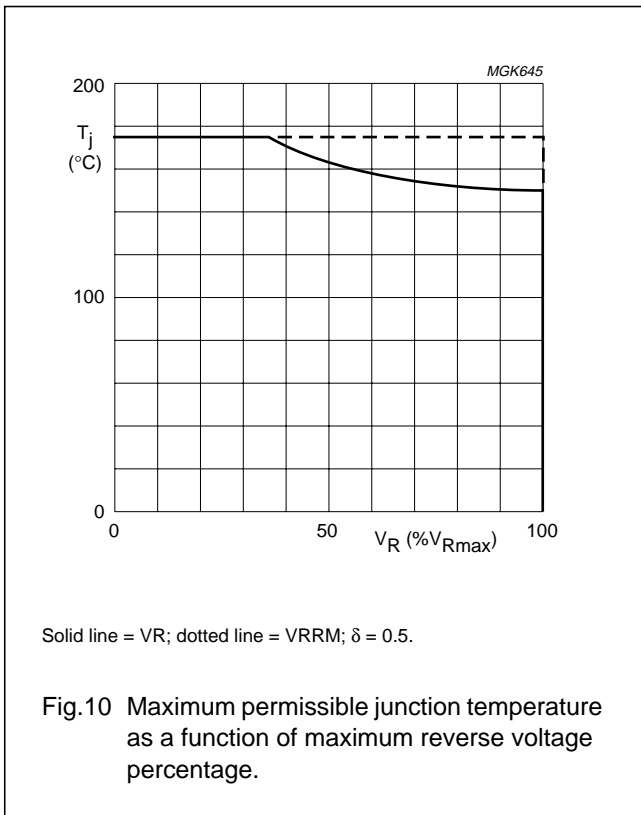
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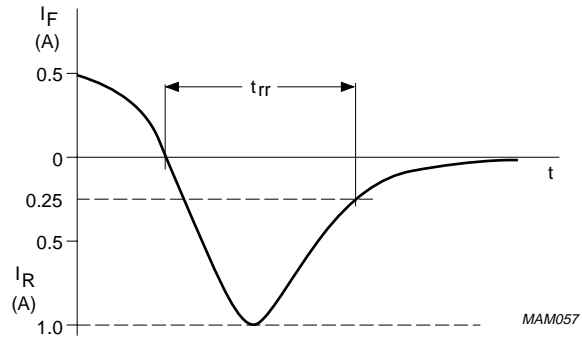
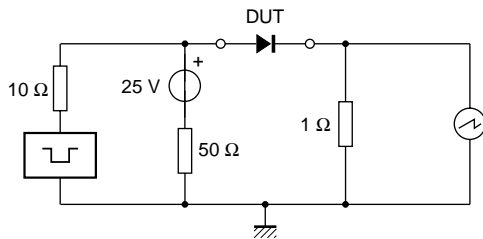
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Ultra fast low-loss controlled avalanche rectifier

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Input impedance oscilloscope: 1 MΩ, 22 pF;  $t_r \leq 7$  ns.  
Source impedance: 50 Ω;  $t_r \leq 15$  ns.

Fig.13 Test circuit and reverse recovery time waveform and definition.



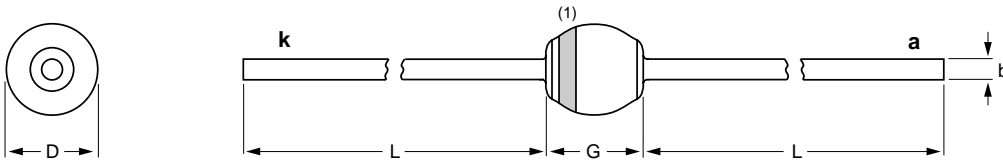
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PACKAGE OUTLINE

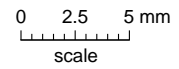
Hermetically sealed glass package; axial leaded; 2 leads

SOD57



DIMENSIONS (mm are the original dimensions)

UNIT	b max.	D max.	G max.	L min.
mm	0.81	3.81	4.57	28



Note

1. The marking band indicates the cathode.

OUTLINE VERSION	REFERENCES			EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ		
SOD57					97-10-14

## Ultra fast low-loss controlled avalanche rectifier

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## DATA SHEET STATUS

LEVEL	DATA SHEET STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)(3)</sup>	DEFINITION
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